



DEC 23 2003

PTO/SB/21 (08-03)

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**TRANSMITTAL
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(to be used for all correspondence after initial filing)

Application Number	10/625,264
Filing Date	July 23, 2003
First Named Inventor	Garrett
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01048US1

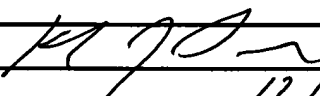
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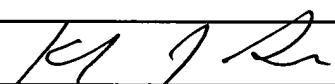
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Firm or Individual name	Kirk J. DeNiro, Esq., Vierra Magen Marcus Harmon & DeNiro LLP
Signature	
Date	12/23/03

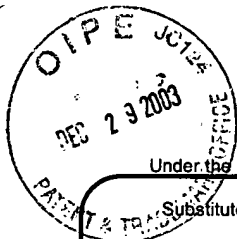
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STATEMENT BY APPLICANT**

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Sheet 1

of 5

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Attorney Docket Number	RAMB-01048US1

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
		US- 3,646,329*	02-29-1972	Yoshino et al.	
		US- 4,192,005*	03-04-1980	Kurtz	
		US- 4,315,210*	02-09-1982	Michel et al.	
		US- 4,329,600*	05-11-1982	Stewart	
		US- 4,414,480*	11-08-1983	Zasio	
		US- 4,549,124*	10-22-1985	Beier	
		US- 4,680,487*	07-14-1987	Kobayashi	
		US- 4,691,127*	09-01-1987	Huizer	
		US- 4,707,620*	11-17-1987	Sullivan et al.	
		US- 4,715,003*	12-22-1987	Keller et al.	
		US- 4,719,369*	01-12-1988	Asano et al.	
		US- 4,728,881*	03-01-1988	Evans et al.	
		US- 4,765,188*	08-23-1988	Krechmery et al.	
		US- 4,779,013*	10-18-1988	Tanaka	
		US- 4,859,877*	08-22-1989	Cooperman et al.	
		US- 5,568,068*	10-22-1996	Ota et al.	
		US- 5,596,285*	01-21-1997	Marbot et al.	
		US- 5,838,177*	11-17-1998	Keeth	
		US- 5,977,797*	Nov. 2, 1999	Gasparik	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP0482392*	02-1991			

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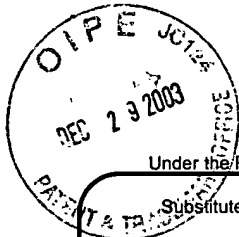
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Sheet 2

of 5

Application Number	10/625,264
Filing Date	July 23, 2003
First Named Inventor	Garrett
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01048US1

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		Number-Kind Code ² (if known)			
		US- 4,860,198*	08-22-1989	Takemaka	
		US- 4,894,562*	01-16-1990	Cavaliere et al.	
		US- 4,939,389*	07-3-1990	Cox et al.	
		US- 4,958,520*	09-25-1990	Trommler et al.	
		US- 4,967,105*	10-30-1990	Akamatsu et al.	
		US- 4,977,333*	12-11-1990	Suzuki et al.	
		US- 4,992,724*	02-12-1991	Hisanaga et al.	
		US- 5,023,488*	06-11-1991	Gunning et al.	
		US- 5,024,101*	06-18-1991	Tanaka et al.	
		US- 5,045,730*	09-03-1991	Cooperman et al.	
		US- 5,045,832*	09-03-1991	Tam	
		US- 5,055,715*	10-8-1991	Inaba	
		US- 5,075,569*	12-24-1991	Branson	
		US- 5,081,379*	01-14-1992	Korteling	
		US- 5,097,148*	03-17-1992	Gabara	
		US- 6,072,747*	06-06-2000	Yoon	
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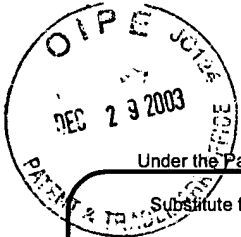
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Sheet 3

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Filing Date	July 23, 2003
First Named Inventor	Garrett
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01048US1

U. S. PATENT DOCUMENTS					
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		Number-Kind Code ² (if known)			
		US- 5,107,230*	04-21-1992	King	
		US- 5,177,130*	05-26-1992	Shoji	
		US- 5,118,971*	06-02-1992	Schenck	
		US- 5,121,064*	06-09-1992	Eller	
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		US- 5,165,046*	11-17-1992	Hesson	
		US- 5,185,538*	02-09-1993	Kondoh et al.	
		US- 5,194,765*	03-16-1993	Dunlop et al.	
		US- 5,206,546*	04-27-1993	Usami	
		US- 5, 237,214*	08-17-1993	Usami	
		US- 5,254,883*	10-19-1993	Horowitz et al.	
		US- 5,296,756*	03-22-1994	Patel et al.	
		US- 5,387,824*	02-07-1995	Michelsen	
		US- 5,457,407*	10-10-1995	Shu et al.	
		US- 5,546,042*	08-13-1996	Tedrow et al.	
		US- 6,442,644	08-27-2002	Gustavson et al.	
		US- 6,009,487	12-28-1999	Davis et al.	
		US-			
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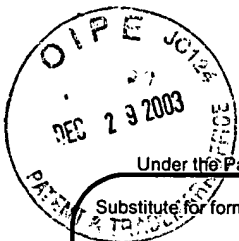
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Sheet 4 of 5

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*Reynolds, C.B., "Analysis and Guidelines for High-Speed VLSI System Interconnections," IEEE 1988 Custom Integrated Circuits Conference, pp. 23.5.1-23.5.4.	
		*Leung, Kam, "Controlled Slew Rate Output Buffer," IEEE 1988 Custom Integrated Circuits Conference, pp. 5.5.1-5.5.4.	
		*Knight, Thomas F. Jr., and Krymm, Alexander, "A Self-Terminating Low-Voltage Swing CMOS Output Driver," Journal of Solid State Circuits, Vol.23, No.2, Apr. 1998, pp. 457-464.	
		*Cox, Dennis T., et al., "VLSI Performance Compensation for Off-Chip Drivers and Clock Generation," IEEE 1989 Custom Integrated Circuits Conf., pp.14.3.1-14.3.4.	
		*Douseki, Takakuni and Ohmori, Yasuo, "BiCMOS Circuit Technology for a High-Speed SRAM," IEEE Journal of Solid-State Circuits, vol. 23, No. 1, Feb. 1988, pp. 68-73.	
		*"Driver with Noise-Dependent Switching Speed Control," IBM Technical Disclosure Bulletin, Vol. 29, No. 3, Aug. 1986, IBM Corporation, pp. 1243-1244.	
		*Biber, Alice I. "The Design of an Application Specific Interface Drive for a High Capacitive Load," Masters Thesis at the Massachusetts Inst. of Technology, Dec. 1989.	
		*Raver, Norman, "Open-Loop Gain Limitations for Push-Pull Off-Chip Drivers," Journal of Solid-State Circuits, Vol. SC-22, No. 2, Apr. 1987, pp.145-150.	
		*Senbon, T. and Hanabuchi, F., Instrumentation Systems: Fundamentals and Applications, Chapter 3: Detection and Conversion of Industrial Variables; 1991, 11 pages.	
		*Johnson, C. and Richeh, H, "Highly Accurate Resistance Deviation to Frequency Converter with Programmable Sensitivity and Resolution," IEEE vol. IM-35, No.2, Jun.1986, 4 pages.	

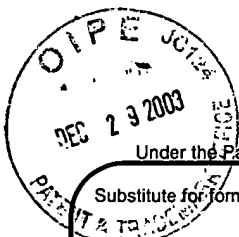
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		*Sasaki, H., et al., "High Precision Automated Resistance Measurement Using A Modified Wheatstone Bridge," CPEM '88 Digest, Japan, 1988, 1 page.	
		*Sasaki, H., et al., "Measurement of the Pressure Dependence of Standard Resistors Using A Modified Wheatstone Bridge," Trans. IEE of Japan, vol. 109, No. 1/2, Jan./Feb. 1989, 6 pages.	
		*Sasaki, H., et al., "Automated Measurement System for 1-OMEGA Standard Resistors Using A Modified Wheatstone Bridge," IEEE Trans. on Instr. and Measurement, vol. 40, No. 2, Apr. '91, p. 274-277.	
		*Sasaki, H., et al., "A Modified Wheatstone Bridge for High-Precision Automated Resistance Measurement," IEEE Trans. on Instr. and Measurement, vol. 26, No. 2, Dec. 1987, p. 947-949.	
		*Armstrong, David H., "Pitfalls in Testing Digital ASIC Devices," IEEE 1987 Custom Integrated Circuits Conference, pp. 573-578.	
		*Mathews, John W. and Erdelyi, Charles K. "Power Supply Voltages for Future VLSI," IEEE 1986 Custom Integrated Circuits Conference, pp. 149-152.	
	✓	Gillingham, Peter, MOSAID Technologies, Inc., "SLDRAM Architectural and Functional Overview", 1997 SLDRAM Consortium, 29 Aug. 1997, pp. 1-14.	
	✓	Gillingham, Peter, and Vogley, Bill, "SLDRAM" High-Performance, Open-Standard Memory", 1997 IEEE, November/December 1997, pp. 29-39	
	✓	Draft 0.99 IEEE P1596.7-199X, "Draft Standard for A High-Speed Memory Interface (SyncLink)", Oct. 14, 1996 IEEE	

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